AMENDMENTS TO THE CLAIMS

Please amend the claims as indicated below. The language being added is underlined ("__") and the language being deleted contains either a strikethrough ("___") or is enclosed by double brackets ("[[]]").

(Currently Amended) A method for scheduling thread execution, comprising:
 maintaining a circular array structure having a plurality of time slots therein,
 wherein each of the plurality of time slots corresponds to a timeslice during which CPU
 resources are allocated to a particular thread;

configuring each time slot in the circular array to include a queue of threads scheduled for execution during that time slot;

maintaining a pointer index for referencing one time slot in the circular array and whereby advancement through the circular array is provided by advancing the pointer index;

maintaining an array of threads requesting immediate CPU resource allocation based on the queue of threads;

suspending a currently executing thread <u>upon expiration of a current timeslice</u> <u>associated with the currently executing thread;</u>

calculating a next time slot during which the currently executing thread should next resume execution;

appending the suspended currently executing thread to the queue of threads scheduled for execution at the calculated time slot;

identifying a next sequential non-empty time slot that includes a queue of threads scheduled for execution during that time slot;

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updating the pointer index to point to the identified next sequential non-empty time slot;

appending any contents of the indexed time slot to the array of threads requesting immediate CPU resource allocation;

removing the thread at the top of the array of threads requesting immediate CPU resource allocation; and

activating the thread at the top of the array of threads requesting immediate CPU resource allocation.

- 2. (Original) The method of claim 1, wherein each timeslice is between 10 and 100 microseconds.
- 3. (Original) The method of claim 1, wherein the array of threads requesting immediate CPU resource allocation includes a first-in-first-out (FIFO) structure.
 - 4. (Canceled)
- 5. (Original) The method of claim 1, wherein the step of suspending a currently executing thread includes:

receiving a self-suspend request from the currently executing thread.

6. (Currently Amended) The method of claim 5, further comprising: suspending the currently executing thread;

advancing the index pointer by one time slot;

allocation is empty;

removing a list of any threads to be executed at the indexed time slot and appending them to the array of threads requesting immediate CPU resource allocation; determining whether the array of threads requesting immediate CPU resource

returning to the step of advancing the index pointer by one slot if it is determined that the array of threads requesting immediate CPU resource allocation is empty does not contain any threads; and

removing and activating the thread at the top of the array of threads requesting immediate CPU resource allocation if it is determined that the array of threads requesting immediate CPU resource allocation is not empty.

7. (Currently Amended) The method of claim 1, further comprising:
receiving an external event interrupt requesting CPU resource allocation for a
new thread;

calculating a next time slot during which the currently executing thread should next resume execution;

determining whether the external event interrupt is requesting immediate CPU resource allocation;

appending the new thread to a queue on a time slot on the circular array if it is determined that the external event interrupt is not requesting immediate CPU resource allocation;

determining [[the]] a type of thread currently executing;

activating the new thread if no thread is currently executing;

performing the following steps if it is determined that a non-idle thread is currently executing:

suspending the currently executing thread;

appending the currently executing thread to the end of the array of threads requesting immediate CPU resource allocation; and

activating the new thread;

performing the following steps if it is determined that an idle thread is currently executing:

suspending the currently executing thread; and activating the new thread.

8. (Currently Amended) A method for scheduling thread execution, comprising: maintaining a plurality of circular array structures associated with a plurality of discrete thread priorities, each having a plurality of time slots therein, wherein each of the plurality of time slots corresponds to a timeslice during which CPU resources are allocated to a particular thread;

configuring each time slot in each of the circular arrays to include a queue of threads scheduled for execution during that time slot;

maintaining at least one pointer index for referencing one time slot in each of the circular arrays, whereby advancement through the circular arrays is provided by

advancing the pointer index;

maintaining an array of threads requesting immediate CPU resource allocation for each of the plurality of circular arrays;

assigning each thread to be executed a specific priority;

incrementing the index pointer by one slot;

removing, for each of the plurality of circular arrays, each queue of threads for the indexed time slot;

appending each removed thread to the array of threads requesting immediate CPU resource allocation associated with its respective circular array;

determining whether the array of threads requesting immediate CPU resource allocation associated with a first circular array is non-empty contains any threads;

proceeding to a next circular array if the array of threads requesting immediate CPU resource allocation is empty;

extracting its top thread if the array of threads requesting immediate CPU resource allocation is non-empty contains any threads;

determining whether a priority of the top thread is greater than a priority of the currently executing thread;

calculating a time for next execution of the top thread if it is determined that the priority of the top thread is not greater than the priority of the currently executing thread;

performing the following steps if it is determined that the priority of the top thread is greater than a priority of the currently executing thread:

suspending the currently executing thread;

activating the top thread; and

calculating the time of next execution for the previously executing suspended thread;

determining whether each of the array of threads requesting immediate CPU resource allocation associated with each of the circular arrays has been processed; and proceeding to the next array of threads requesting immediate CPU resource allocation if it is determined that not all arrays of threads requesting immediate CPU resource allocation have been processed.

- 9. (Original) The method of claim 8, wherein each of the plurality of circular arrays corresponds to one of four assigned priority levels: a non-real-time priority; a soft-real-time priority; a hard-real-time priority; and a critical-real-time priority.
 - 10. (Original) The method of claim 8, further comprising:

receiving an external event interrupt requesting CPU resource allocation for a new thread:

calculating a next time slot during which the currently executing thread should next resume execution;

determining whether the external event interrupt is requesting immediate CPU resource allocation;

appending the new thread to a queue on a time slot on the circular array if it is determined that the external event interrupt is not requesting immediate CPU resource allocation;

determining whether a priority of the new thread is greater than a priority of the

currently executing thread;

appending the new thread to the end of the array of threads requesting immediate CPU resources for the associated priority if it is determined that the priority of the new thread is not greater than the priority of the currently executing thread; and performing the following steps if it is determined that the priority of the new thread is greater than the priority of the currently executing thread:

suspending the currently executing thread;

calculating the time for next execution for the currently executing thread appending the currently executing thread to array associated with the calculated time slot; and

activating the new thread.

11. (Currently Amended) A method for scheduling thread execution, comprising: maintaining a circular array structure having a plurality of time slots therein, wherein each of the plurality of time slots corresponds to a timeslice during which CPU resources are allocated to a particular thread;

configuring each time slot in the circular array to include a queue of threads scheduled for execution during that time slot;

maintaining a pointer index for referencing one time slot in the circular array and whereby advancement through the circular array is provided by advancing the pointer index;

maintaining an array of threads requesting immediate CPU resource allocation based on the queue of threads;

calculating a next time slot during which a currently executing thread should next resume execution;

appending the currently executing thread to the queue of threads scheduled for execution at the calculated time slot;

identifying a next sequential non-empty time slot containing a queue of threads scheduled for execution during that time slot;

updating the pointer index to point to the identified next sequential non-empty time slot;

appending any contents of the indexed time slot to the array of threads requesting immediate CPU resource allocation;

removing the thread at the top of the array of threads requesting immediate CPU resource allocation;

determining whether the thread at the top of the array of threads requesting immediate CPU resource allocation is identical to the currently executing thread;

maintaining execution of the currently executing thread for the following time slot if it is determined that the thread at the top of the array of threads requesting immediate CPU resource allocation is identical to the currently executing thread;

suspending a currently executing thread; and

activating the thread at the top of the array of threads requesting immediate CPU resource allocation if it is determined that the thread at the top of the array of threads requesting immediate CPU resource allocation is not identical to the currently executing thread.

12. (Currently Amended) A computer-readable <u>storage</u> medium <u>storing a</u>

<u>program incorporating instructions</u> for scheduling thread execution, <u>the program causing</u>

<u>a computer to perform comprising</u>:

one or more instructions for maintaining a circular array structure having a plurality of time slots therein, wherein each of the plurality of time slots corresponds to a timeslice during which CPU resources are allocated to a particular thread;

ene or more instructions for configuring each time slot in the circular array to include a queue of threads scheduled for execution during that time slot;

one or more instructions for maintaining a pointer index for referencing one time slot in the circular array and whereby advancement through the circular array is provided by advancing the pointer index;

one or more instructions for maintaining an array of threads requesting immediate CPU resource allocation based on the queue of threads;

expiration of a current timeslice associated with the currently executing thread;

executing thread should next resume execution;

ene or more instructions for appending the suspended currently executing thread to the queue of threads scheduled for execution at the calculated time slot;

one or more instructions for identifying a next sequential non-empty time slot that includes a queue of threads scheduled for execution during that time slot;

ene or more instructions for updating the pointer index to point to the identified next sequential non-empty time slot;

ene or more instructions for appending any contents of the indexed time slot to the array of threads requesting immediate CPU resource allocation;

ene or more instructions for removing the thread at the top of the array of threads requesting immediate CPU resource allocation; and

one or more instructions for activating the thread at the top of the array of threads requesting immediate CPU resource allocation.

- 13. (Currently Amended) The computer-readable <u>storage</u> medium of claim 12, wherein each timeslice is between 10 and 100 microseconds.
- 14. (Currently Amended) The computer-readable <u>storage</u> medium of claim 12, wherein the array of threads requesting immediate CPU resource allocation includes a first-in-first-out (FIFO) structure.

15. (Canceled)

16. (Currently Amended) The computer-readable <u>storage</u> medium of claim 12, wherein the one or more instructions for suspending a currently executing thread further <u>comprises</u> include: one or more instructions for receiving a self-suspend request from the currently executing thread.

17. (Currently Amended) The computer-readable <u>storage</u> medium of claim 16, further causing the computer to perform comprising:

one or more instructions for suspending the currently executing thread;
one or more instructions for advancing the index pointer by one time slot;
one or more instructions for removing a list of any threads to be executed at the indexed time slot and appending them to the array of threads requesting immediate
CPU resource allocation;

one or more instructions for determining whether the array of threads requesting immediate CPU resource allocation is empty;

one or more instructions for returning to the step of advancing the index pointer by one slot if it is determined that the array of threads requesting immediate CPU resource allocation is empty does not contain any threads; and

one or more instructions for removing and activating the thread at the top of the array of threads requesting immediate CPU resource allocation if it is determined that the array of threads requesting immediate CPU resource allocation is not empty.

18. (Currently Amended) The computer-readable storage medium of claim 12, further causing the computer to perform comprising: one or more instructions for receiving an external event interrupt requesting CPU resource allocation for a new thread;

executing thread should next resume execution;

one or more instructions for determining whether the external event interrupt is

requesting immediate CPU resource allocation;

on the circular array if it is determined that the external event interrupt is not requesting immediate CPU resource allocation;

one or more instructions for determining the type of thread currently executing;

one or more instructions for activating the new thread if no thread is currently executing;

ene or more instructions for performing the following steps if it is determined that a non-idle thread is currently executing:

suspending the currently executing thread;

appending the currently executing thread to the end of the array of threads requesting immediate CPU resource allocation; and

activating the new thread;

one or more instructions for performing the following steps if it is determined that an idle thread is currently executing:

suspending the currently executing thread; and activating the new thread.

19. (Currently Amended) A computer-readable <u>storage</u> medium <u>storing a</u>

<u>program incorporating instructions</u> for scheduling thread execution, <u>the program causing</u>

<u>a computer to perform-comprising</u>:

one or more instructions for maintaining a plurality of circular array structures associated with a plurality of discrete thread priorities, each having a plurality of time

slots therein, wherein each of the plurality of time slots corresponds to a timeslice during which CPU resources are allocated to a particular thread;

ene or more instructions for configuring each time slot in each of the circular arrays to include a queue of threads scheduled for execution during that time slot;

one time slot in each of the circular arrays, whereby advancement through the circular arrays is provided by advancing the pointer index;

ene or more instructions for maintaining an array of threads requesting immediate CPU resource allocation for each of the plurality of circular arrays;

ene or more instructions for assigning each thread to be executed a specific priority;

one or more instructions for incrementing the index pointer by one slot;

one or more instructions for removing, for each of the plurality of circular arrays,
each queue of threads for the indexed time slot;

one or more instructions for appending each removed thread to the array of threads requesting immediate CPU resource allocation associated with its respective circular array;

one or more instructions for determining whether the array of threads requesting immediate CPU resource allocation associated with a first circular array is non-empty contains any threads;

ene or more instructions for proceeding to a next circular array if the array of threads requesting immediate CPU resource allocation is empty;

ene or more instructions for extracting its top thread if the array of threads

requesting immediate CPU resource allocation is non-empty contains any threads;

one or more instructions for determining whether a priority of the top thread is greater than a priority of the currently executing thread;

one or more instructions for calculating a time for next execution of the top thread if it is determined that the priority of the top thread is not greater than the priority of the currently executing thread;

ene or more instructions for performing the following steps if it is determined that the priority of the top thread is greater than a priority of the currently executing thread:

suspending the currently executing thread;

activating the top thread; and

calculating the time of next execution for the previously executing thread;

ene or more instructions for determining whether each of the array of threads requesting immediate CPU resource allocation associated with each of the circular arrays has been processed; and

one or more instructions for proceeding to the next array of threads requesting immediate CPU resource allocation if it is determined that not all arrays of threads requesting immediate CPU resource allocation have been processed.

20. (Currently Amended) The computer-readable <u>storage</u> medium of claim 19, wherein each of the plurality of circular arrays corresponds to one of four assigned priority levels: a non-real-time priority; a soft-real-time priority; a hard-real-time priority; and a critical-real-time priority.

21. (Currently Amended) The computer-readable <u>storage</u> medium of claim 19, <u>the program further causing the computer to perform comprising</u>:

one or more instructions for receiving an external event interrupt requesting CPU resource allocation for a new thread;

executing thread should next resume execution;

one or more instructions for determining whether the external event interrupt is requesting immediate CPU resource allocation;

one or more instructions for appending the new thread to a queue on a time slot on the circular array if it is determined that the external event interrupt is not requesting immediate CPU resource allocation;

one or more instructions for determining whether a priority of the new thread is greater than a priority of the currently executing thread;

one or more instructions for appending the new thread to the end of the array of threads requesting immediate CPU resources for the associated priority if it is determined that the priority of the new thread is not greater than the priority of the currently executing thread; and

ene or more instructions for performing the following steps if it is determined that the priority of the new thread is greater than the priority of the currently executing thread:

suspending the currently executing thread;

calculating the time for next execution for the currently executing thread appending the currently executing thread to array associated with the calculated time slot; and

activating the new thread.

22. (Currently Amended) A computer-readable <u>storage</u> medium <u>storing a</u>

<u>program incorporating instructions</u> for scheduling thread execution, <u>the program causing</u>

a computer to perform <u>comprising</u>:

ene or more instructions for maintaining a circular array structure having a plurality of time slots therein, wherein each of the plurality of time slots corresponds to a timeslice during which CPU resources are allocated to a particular thread;

ene or more instructions for configuring each time slot in the circular array to include a queue of threads scheduled for execution during that time slot;

ene or more instructions for maintaining a pointer index for referencing one time slot in the circular array and whereby advancement through the circular array is provided by advancing the pointer index;

one or more instructions for maintaining an array of threads requesting immediate CPU resource allocation;

executing thread should next resume execution;

ene or more instructions for appending the currently executing thread to the queue of threads scheduled for execution at the calculated time slot;

one or more instructions for identifying a next sequential non-empty time slot;

one or more instructions for updating the pointer index to point to the identified next sequential non-empty time slot;

one or more instructions for appending any contents of the indexed time slot to

the array of threads requesting immediate CPU resource allocation;

ene or more instructions for removing the thread at the top of the array of threads requesting immediate CPU resource allocation;

one or more instructions for determining whether the thread at the top of the array of threads requesting immediate CPU resource allocation is identical to the currently executing thread;

one or more instructions for maintaining execution of the currently executing thread for the following time slot if it is determined that the thread at the top of the array of threads requesting immediate CPU resource allocation is identical to the currently executing thread;

one or more instructions for suspending a currently executing thread; and one or more instructions for activating the thread at the top of the array of threads requesting immediate CPU resource allocation if it is determined that the thread at the top of the array of threads requesting immediate CPU resource allocation is not identical to the currently executing thread.